



**Indian Institute of Technology, Hyderabad,
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Date:14/01/2020

WALK-IN INTERVIEW FOR THE POST OF PROJECT OFFICER-I ON ADHOC BASIS

IIT Hyderabad, EE department under the project “Design of Dynamic MAC and PHY SoC for Low Power and Long Range Networks” is conducting Walk-in-Interviews (preceded by a written test) at IIT Hyderabad, Kandi Campus. (Location and direction: Visit www.iith.ac.in) on **4th February, 2020 at B-602.**

Candidates fulfilling the eligibility criteria and experience may **WALK-IN** directly with the duly filled application in the prescribed format and bio data along with Proof of age, qualifications and experience (and also send resume chaithanya@iith.ac.in by **1st February, 2020**)

Post: Project Associate

Nature of Post: The appointment is purely on ADHOC basis and may be terminated at any time with one-month notice by the Project Head, IITH.

No. of Post: 02 (Two)

Qualification:

- M.E./M.Tech from any IIT in EE/ECE (65% or above in both UG & PG)
- PhD (Awarded or thesis submitted) in EE/ECE (65% or above in both UG & PG)

Experience:

3+ years' experience in VLSI Analog Chip design / Microwave Design/ Communication System Design

Age: Not exceeding 35 years

Emoluments Range (INR): Rs. 30,000-55,000 per month.

Tenure: Contract for a period of eleven months and may be extended based on performance and requirement.

Ashudeb Dutta