



# DELHI TECHNOLOGICAL UNIVERSITY

Established by Govt. of Delhi vide Act 6 of 2009  
(FORMERLY DELHI COLLEGE OF ENGINEERING)  
BAWANA ROAD, DELHI-110042

Advt. No. DTU-MeitY-MCEV/EED/01

11 October 2019

## ADVERTISEMENT TO FILL UP PROJECT POSITIONS

A walk in Interview for filling of project positions as per the details given below for the MeitY Sponsored R&D Project under Chief Investigator Dr. Mini Sreejeth, Electrical Engineering Department (EED), Delhi Technological University (DTU) and Co-investigator Prof. Indu S, Department of Electronics and Communication Engineering, DTU is scheduled to be held on 22 Oct 2019 in the Committee Room of the Electrical Engineering Department.

- Title of the Project** : Development and Deployment of motor controller for low to medium Power Electric Vehicles
- Sponsoring Agency** : Ministry of Electronics and Information Technology (MeitY)
- Details of Project Positions**

Sl. No	Désignation	Minimum Qualification	Experience	Nos.	Monthly Emoluments (₹)
a)	Engineer	B.E. / B. Tech. in Electrical Engineering / Electrical and Electronics Engineering / Electronics and Communication Engineering and NET/ GATE Qualified <b>OR</b> M.E. / M. Tech. (Power Electronics) with 1 <sup>st</sup> Division in both B.E. and M.E. levels	At least 5 years industrial experience in design and development of Inverters	2	40,000
b)	SRF	M.E. / M. Tech. (Power Electronics) with 1 <sup>st</sup> Division in both B.E./B.Tech and M.E. levels	Experience in power electronics and electronics hardware design	1	28,000
c)	JRF	B.E. / B. Tech. in Electrical Engineering / Electrical and Electronics Engineering / Electronics and Communication Engineering <b>OR</b> M.E. / M. Tech. (Power Electronics) with 1 <sup>st</sup> Division in both B.E. and M.E. levels  Preferably NET/ GATE qualified	Major Project (B.E./B.Tech/M.E./M. Tech) in Power Electronics Area	1	25,000

- Age Limit** :
  - 40 years, for Sl. No. a), relaxable up-to 5 years for candidates belonging to SC/ST/PH/OBC categories.
  - 35 years, for Sl. No. b) & c) relaxable up-to 5 years for candidates belonging to SC/ST/PH/OBC categories.

Professor & Head  
Electrical Engineering Department  
Delhi Technological University



5. **Duration** : Appointment to the above post shall initially be for a period of six months, with further extension(s) of six month, based on performance, if deemed necessary, or for the duration of the project, whichever is earlier.

6. **Job description**

a)	Engineer	:	<ul style="list-style-type: none"> <li>- Embedded software development on 32 bits microcontroller platform for Inverter products</li> <li>- Software debugging</li> <li>- Excellent team work</li> <li>- Must have Experience on RS232, RS485 and CAN bus communication</li> </ul>
b)	SRF	:	<ul style="list-style-type: none"> <li>- Electronics Hardware Development for power product.</li> <li>- Circuit schematic development</li> <li>- Circuit simulation</li> <li>- Well versed in components selection</li> <li>- Hardware debugging</li> <li>- Must be able to work with CRO, Multimeters, power supplies, signal generators, multimeters.</li> <li>- Bill of Material preparation</li> </ul>
c)	JRF	:	<ul style="list-style-type: none"> <li>- Electronics Hardware Development for power product.</li> <li>- Circuit schematic using software tools</li> <li>- Hardware debugging</li> <li>- Must be able to work with CRO, Multimeters, power supplies, signal generators, multimeters.</li> <li>- Bill of Material preparation</li> </ul>

7. Candidates before appearing for the Walk in Interview shall ensure that they are eligible for the position they intend to apply.

8. Candidates applying for the above post are requested to report on 22 October 2019 at 10 am for registration, verification of documents, test / interview in the Department of Electrical Engineering DTU. Candidates reporting after 11:00 am will not be entertained.

9. Candidates desirous to appear for the Walk in Interview should bring a set of self-attested copies of all certificates, testimonials, the latest CV and a passport sized colour photograph. The Application should include: -

- Details of degree/certificates obtained starting from Class X (or equivalent) clearly indicating the aggregate percentage / CGPA
- Experience in research, industrial field, academics and others.

10. Candidate shall bring along with them the original degree(s)/certificate(s) and experience certificate(s) at the time of walk in interview for verification.

11. Please note that no TA/DA is admissible for attending the walk-in interview.

12. The interview will be held in Committee Room of Electrical Engineering Department on 22 Oct 2019 at 10 am .

13. The competent authority reserves the right to reject the candidature of any or all of the candidates without assigning any reason therefor.

*Indu*  
Prof. Indu  
Co- Investigator, Project

*K*  
11/10/19  
Professor & Head  
Electrical Engineering Department  
Delhi Technological University  
(Formerly Delhi College of Engineering)  
Bawana Road, Delhi-110042

*Mini Sreejeth*  
11/10/19  
Dr. Mini Sreejeth  
Chief Investigator, Project